

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number: 7,060,627
Issued: 6/13/2006
Name of Patentee: Micha Gutman, et al.
Title of Invention: METHOD OF DECREASING CHARGING EFFECTS IN OXIDE-NITRIDE-
OXIDE (ONO) MEMORY ARRAYS

Attention: Certificate of Corrections Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT
FOR APPLICANT'S MISTAKE (37 C.F.R. section 1.323)

1. It is noted that an error appears in this patent of a typographical nature or character, as more fully described below. It occurred in good faith. Correction thereof does not involve such changes in the patent as would constitute new matter or would require re-examination. A certificate of correction is requested.
2. Attached hereto is Form PTO-1050 with at least one copy being suitable for printing.
3. The exact page and line number where the error occurs in the application file are:

In the assignment recorded March 1, 2004, all inventors assigned their rights in the above invention jointly to Tower Semiconductor Ltd. and Matsushita Electronic Components Co., Ltd. This assignment was recorded in the USPTO on March 1, 2004 at Reel: 014387, Frame: 0973 before the issuance of the patent. The above-referenced patent is co-owned by Tower Semiconductor Ltd. and Matsushita Electronic Components Co., Ltd.

4. Please send the Certificate to the undersigned at Customer No.: 33726.
5. Please pay the fee required by 37 C.F.R. § 1.20(a) and the processing fee required by 37 C.F.R. § 1.17(i) as follows:

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/E.ERIC HOFFMAN/

E. Eric Hoffman
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Bever, Hoffman & Harms, LLP

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 7,060,627

APPLICATION NO.: 10659,042

ISSUE DATE : June 13, 2006

INVENTOR(S) : Micha Gutman et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Correct:

(73) Assignee: Tower Semiconductor Ltd., Migdal Haemek (IL)

TO:

(73) Assignee: Tower Semiconductor Ltd., Migdal Haemek (IL); Matsushita Electronic Components Co., Ltd., aka Matsushita Electric Industrial Co., Ltd., Osaka (JP)

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Bever, Hoffman & Harms, LLP
901 Campisi Way, Suite 370
Campbell, CA 95008

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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